Attorney Docket: 1941/164

METHOD AND SYSTEM FOR MULTIPLICATION OF BINARY NUMBERS

ABSTRACT

A multiplier for multiplying a first signal representing a first binary number $a = [a_{N-1} \ldots a_1 \, a_0]$ and a second signal representing a second binary number $b = [b_{N-1} \ldots b_1 \, b_0]$. The multiplier includes a first port for receiving the first signal, and a second port for receiving the second signal. A triangle array is operatively coupled to the first signal and the second signal. An adder adds elements of the triangle array to produce a third signal representing a product of the first signal and the second signal.